

Amendment to the Specification

Please amend the specification as follows.

Please amend the paragraph beginning at pages 9-10, line 11, as follows:

The CPU 11 obtains the temperature states of the respective driver ICs by monitoring the temperature flags outputted from the comparison circuits 13a, 13b and 13c. On the basis of the temperature flags, when the driver ICs have higher temperatures than the respective arbitrarily set temperatures, a control signal is outputted to the DSP 10 so that the driver ICs 7, 8 and 9 do not generate any more heat, respectively. The DSP 10 which has received the control signal controls the revolution of the disk 1 together with the CPU 11, through the spindle driver IC 7, the traverse driver IC 8 or the actuator driver IC 9. For example, when the temperature of the spindle driver IC 7 becomes equal to or higher than the arbitrarily set temperature, the CPU 11 and the DSP 10 control the spindle driver IC 7 so that forced acceleration or forced deceleration of the disk 1 is not performed for an arbitrary period of time, whereby the motion of the spindle motor 2 is eased, and heat generation of the spindle driver IC 7 is suppressed. Or, a ~~free-run~~ run state of the disk 1 is included with changes in revolution of the disk 1, thereby suppressing the heat generation. Further, when the temperature of the traverse driver IC 8 becomes equal to or higher than the arbitrarily set temperature, a high-speed movement of traverse is eased while the pickup 4 is being on the traverse (thread) move to a target position, thereby suppressing heat generation of the traverse driver IC 8. Further, when the temperature of the actuator driver IC 9 becomes equal to or higher than the arbitrarily set temperature, the spindle driver IC 7 is controlled to reduce the number of revolutions of the disk 1, whereby the face wobbling or eccentricity of the disk 1 is lessened and the motion of the actuator for driving and controlling the object lens 3 which faces the disk 1 is eased, thereby suppressing heat generation of the actuator driver IC.